

SYMBOL TIME SYNCHRONIZER

FIG. 1B

Sheet 2 of 6
Appl. No. 09/696,525
Amdt. dated September 29, 2004
Reply to Office action of May 4, 2004

**Annotated Sheet Showing Changes** ODD TIMING ERROR DETECTOR 24 44a 48a 15 **EARLY** (2k+1)NIN PHASE LATE -e<sub>2k+1</sub> COMPONENT ر36 **GATE ISOLATOR** ⟨50a 47a 46a ODD  $h_D(t)$ **DATA** 55 53ع 54) **MODULO** DATA LOOP Ď€MUX FILTER COUNTER **EVEN**  $h_D(t)$ DATA 46b 50b) **EARLY** QUADRATURE  $e_{2k}$ 2kN LATE COMPONENT **GATE ISOLATOR** 52b' 44b

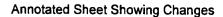
SYMBOL TIME SYNCHRONIZER

26

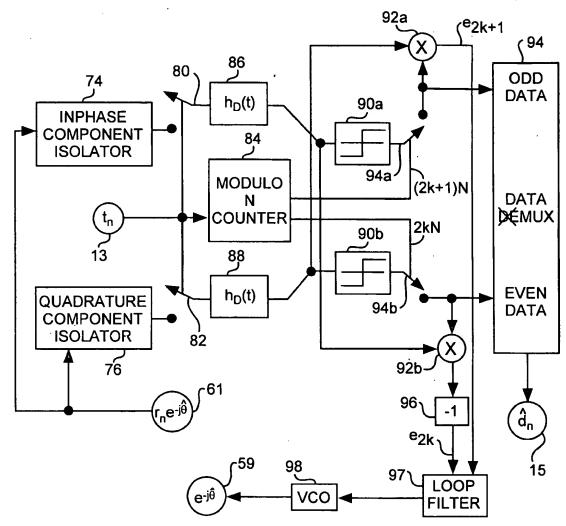
FIG. 1B

**EVEN TIMING ERROR DETECTOR:** 

Sheet 4 of 6 Appl. No. 09/696,525 Amdt. dated September 29, 2004 Reply to Office action of May 4, 2004







CARRIER PHASE SYNCHRONIZER FIG. 2B